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Docket No.: 241189US2S

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

RE: Application Serial No.: 10/634,917

Applicants: Wataru SAITO, et al.

Filing Date: August 6, 2003

For: POWER SEMICONDUCTOR DEVICE USED FOR

POWER CONTROL Group Art Unit: 2822

Examiner: LEWIS, M.

SIR:

Attached hereto for filing are the following papers:

ELECTION

Our check in the amount of \$0.00 is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,

MAJER & NEUSTADT, P.C.

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IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

WATARU SAITO ET AL

: EXAMINER: LEWIS, M.

SERIAL NO: 10/634,917

FILED: AUGUST 6, 2003

: GROUP ART UNIT: 2822

FOR: POWER SEMICONDUCTOR

DEVICE USED FOR POWER CONTROL

ELECTION

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SIR:

In response to the Election Requirement dated September 10, 2004, Applicants elect without traverse Embodiment, Claims 1-25, directed to a power semiconductor device having a first semiconductor layer, a second semiconductor layer, first and second main electrodes formed on the second layer separately from each other, control electrode and third semiconductor layer formed between the control and second main electrode.

Accordingly, examination on the merits of Claims 1-25 is believed to be in order and an early and favorable action is respectfully requested.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,

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